

ABSTRACT

A first video signal generation circuit newly generates pixels between pixels in a first progressive video field signal outputted by a first progressive video generation circuit, and outputs a third progressive video field signal. A second video signal generation circuit newly generates pixels between pixels in a second progressive video field signal outputted by a second progressive video generation circuit, and outputs a fourth progressive video field signal. A comparison circuit compares the third progressive video field signal and the fourth progressive video field signal, and outputs the result of the comparison as motion amount information. An output circuit synthesizes an intra-field interpolation signal and a frame interfiled interpolation signal on the basis of the motion amount information, and outputs a composite signal as a progressive video signal.